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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Georg Rudolf SILLNER

Group Art Unit:

Serial No.:

Examiner:

Filed :

For : **METHOD FOR PROCESSING ELECTRICAL
PARTS, PARTICULARLY FOR PROCESSING
SEMICONDUCTOR CHIPS AND ELECTRICAL
COMPONENTS, AND DEVICE FOR CARRYING
OUT SAID METHOD**

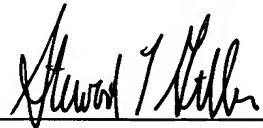
INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with applicant's duty of disclosure, applicant submits a listing of documents on PTO Form 1449 which the Examiner may find relevant to the examination of the application. These documents were listed in the International Search Report (copy enclosed). A copy of each of the foreign documents listed is enclosed.

Respectfully submitted,



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November 8, 2004

Attorney Docket No: A-9311.IDS/cat

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)
A-9311

Application Number
10/512086

Applicant(s)
Georg Rudolf SILLNER
#3

Filing Date

Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A	5,232,143	08/03/1993	Buxton			
	B	6,283,693	09/04/2001	Acello et al			

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	C	2002/0046450	04/25/2002	Odashima et al			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
	D	6216169	08/05/1994	Japan			✓	
	E	2003109979	04/11/2003	Japan			✓	

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.